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Sheet 1

of 1

Complete if Known	
Application Number	10/502,183
Filing Date	July 21, 2004
First Named Inventor	Hendricus Veendrick
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	NL020066

U. S. PATENT DOCUMENTS

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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV.7-80) PATENT AND TRADEMARK OFFICE		Atty. Docket No. NL020066	Serial No. 10/502183
		Applicant HENDRICUS J.M. VEENDRICK ET AL	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Filing Date CONCURRENTLY	Group

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Ex. Int.		Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
DJC	AA	5 7 4 4 9 9 6	4/98	KOTZLE ET AL	327	534	
	AB	5 6 1 2 6 4 5	3/97	HALEPETE	327	537	
	AC	6 2 3 2 8 2 5	5/01	MANNING	327	534	
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							Yes	No
	AG							
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	AI							
	AJ							
	AK							

OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

AL		"A 0.25um CMOS 0.9 V 100 MHz DSP Core" M. Izumikawa et al, IEEE Journal of Solid-State Circuits Vol. 32, No. 1, Jan. 1997
AM		
AN		

Examiner Douglas J. Clegg Date Considered 9/23/2005

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HENDRICUS J.M. VEENDRICK ET AL

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